EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	718	(712/227).CCLS.	US-PGPUB; USPAT	OR	OFF	2007/09/20 10:01
L2	0	(((multi?thread$3) with pipelin$3 with stag$3) same control$4) and (RAM and (boot near4 ROM)) and (peripheral$1 with wait$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/09/20 10:03
L3	0	(((multi?thread$3) with pipelin$3 with stag$3) same control$4) and (RAM and (boot near4 ROM)) and (peripheral$1 with wait$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/09/20 10:03
L4	0	(((multi?thread$3) with pipelin$3 with stag$3) same control$4) and (RAM and (ROM)) and (peripheral$1 with wait$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR		2007/09/20 10:03
L5	0	(((multi?thread$3) with pipelin$3 with stag$3) same control$4) and (peripheral$1 with wait$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/09/20 10:03
L6	0	(((multi?thread$3) with pipelin$3 with stag$3) same control$4) and (peripheral$1 same wait$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/09/20 10:04
L7	1	(((multi?thread$3) with pipelin$3 with stag$3) same control$4) and ((main near4 memor$3) same wait$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/09/20 10:05
L8	1	(((multi?thread$3) with pipelin$3) same control$4) and ((main near4 memor$3) same wait$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/09/20 10:15
L9	0	(((multi?thread$3) with pipelin$3) same control$4) and ((main near4 memor$3) same stall$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/09/20 10:15
L10	17	((multi?thread$3) with control$4) and ((main near4 memor$3) same stall$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/09/20 10:36
L11	0	multi?thread$3 with (round near4 robin$1) with (delay$3 or wait$3 or stall$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/09/20 10:38
L12	335	multi?thread$3 with ((round near4 robin$1) sa,e (delay$3 or wait$3 or stall$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/09/20 10:38

EAST Search History

L13	0	multi?thread$3 with ((round near4 robin$1) same (delay$3 or wait$3 or stall$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/09/20 10:38
L14	8	multi?thread$3 same ((round near4 robin$1) same (delay$3 or wait$3 or stall$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/09/20 10:44
L15	41	multi?thread$3 same ((round near4 robin$1) and (delay$3 or wait$3 or stall$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/09/20 11:24
L16	0	((fine?grain) near4 (multi?thread$3)) same ((round near4 robin$1) and (delay$3 or stall$3 or wait$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/09/20 13:09
L17	1	((fine?grain$3) near4 (multi?thread$3)) same ((round near4 robin$1) and (delay$3 or stall$3 or wait$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/09/20 13:10
L18	1	((fine?grain$3) with (multi?thread$3)) same ((round with robin$1) and (delay$3 or stall$3 or wait$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/09/20 13:17
L19	1	((fine?grain$3) same (multi?thread$3)) same ((round same robin$1) and (delay$3 or stall$3 or wait$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/09/20 13:17
L20	4	((gosior-jason$) and (broughton-colin$) and (jacobsen-phillip$) and (sobota-john$)).in.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/09/20 13:18
L21	0	((multi?thread$3) with (pipelin$3) with control$3 with memory with peripheral with clock$3 with wait$3).clm.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/09/20 13:31
L22	1	((multi?thread$3) with (pipelin$3) with control$3 with wait$3).clm.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/09/20 13:31
L23	0	((multi?thread$3) with (pipelin$3) with control$3 with memory with peripheral with clock$3 with wait$3).clm.	US-PGPUB; USPAT	OR	OFF	2007/09/20 13:31
L24	1	((multi?thread$3) with (pipelin$3) with control$3 with wait$3).clm.	US-PGPUB; USPAT	OR	OFF	2007/09/20 13:31
L25	0	((multi?thread$3) with peripheral$1 with wait with clock$3).clm.	US-PGPUB; USPAT	OR	OFF	2007/09/20 13:55

L26	0	((multi?thread$3) with peripheral$1 with delay$3 with clock$3).clm.	US-PGPUB; USPAT	OR	OFF	2007/09/20 13:56
L27	0	((multi?thread$3) with peripheral$1 with wait$3 with clock$3).clm.	US-PGPUB; USPAT	OR	OFF	2007/09/20 13:56
L28	0	((multi?thread$3) with memor$ with wait$3 with clock$3).clm.	US-PGPUB; USPAT	OR	OFF	2007/09/20 14:10
L29	5	((multi?thread$3) with (wait$3 or delay$3 or stall$3) with (clock$3 or tim$3 or count$3)).clm.	US-PGPUB; USPAT	OR	OFF	2007/09/20 14:10
S2	6	(("5933627") or ("4556951") or ("6101569") or ("6272616") or ("5784552") or ("4155115")).PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/01/06 15:23
S3	4	(bit$1 adj1 pattern$1) near4 (cod$3 near4 protect$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/01/07 11:23
S4	0	(bit$1 adj1 pattern$1) near4 (instruction$1 near4 protect$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/01/07 12:04
S5	512	(712/227).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/01/07 12:04
S6	580	(712/227).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/09/19 07:57
S7	580	(712/227).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/09/21 14:23
S8	610	(712/227).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/04/25 14:49
S9	6	(simultaneous near4 multi?thread$3) with pipelin$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/04/25 14:52
S10	6	(("5933627") or ("4556951") or ("6101569") or ("5272616") or ("5784552") or ("4155115")).PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/04/25 14:54

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S11	0	(multi?thread$3 with pipelin$3) with (robin)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/04/26 16:12
S12	2	(multi?thread$3 same pipelin$3) same (round near4 robin)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/04/26 16:34
S13	104	(thread$3 same pipelin$3) same (round near4 robin)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/04/26 16:34
S14	22	(thread$3 with pipelin$3) with (round near4 robin)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/04/26 16:35
S15	83	(thread$3 with pipelin$3) same (round near4 robin)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/04/26 16:35
S16	33	(thread$3 with pipelin$3) same ((round near4 robin) with schedul$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/04/26 16:37
S17	5	(thread$3) same ((round near4 robin) with schedul$3 with pipelin$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/04/26 16:45
S18	5	(thread$3) same ((round with robin) with schedul$3 with pipelin$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/04/26 16:46
S19	26	(thread$3) same ((round with robin) same (schedul$3 with pipelin$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/04/26 16:49
S20	48	(thread$3) same ((round with robin) same (schedul$3 same pipelin$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/04/26 16:49
S21	661	(712/227).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/09/20 10:01
S22	7	(simultaneous near4 multi?thread$3) with pipelin$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/01/19 13:55

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S23	0	((fin$3) near4 (mutli?thread$3)) with pipelin$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/01/19 13:57
S24	0	((fin$3) with (mutli?thread$3)) with pipelin$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/01/19 13:57
S25	0	((fin$3) with (mutli?thread$3)) same pipelin$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/01/19 13:57
S26	0	((finely?grain$3) with (mutli?thread$3)) same pipelin$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/01/19 13:57
S27	0	((finely) with (multi?thread$3)) same pipelin$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/01/19 13:57
S28	0	((finely) with (multithread$3)) same pipelin$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/01/19 13:58
S29	42	((coarse$3) with (multithread$3)) same pipelin$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/01/19 13:58
S30	75	((fine$3) with (multithread$3)) same pipelin$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/01/19 13:58
S31	54	((fine$3) near4 (multithread$3)) same pipelin$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/01/19 13:59
S32	11	((fine$3) near4 (multithread$3)) with pipelin$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/01/19 13:58

| multithreaded + fine-grained + "round robin" + | Search |

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processor, or one that is heavily ... an event, it must first **wait** for all ...
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KM Kavi, B Lee, AR Hurson - Advances in Computers, 1998 - eecs.oregonstate.edu
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GT Byrd, MJ Flynn - Proceedings of the IEEE, 1999 - ieeexplore.ieee.org
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HC Hsiao, CT King - doi.ieeecomputersociety.org
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Using fine grain multithreading for energy efficient computing
A Gontmakher, A Mendelson, A Schuster - Proceedings of the 12th ACM SIGPLAN symposium on Principles ..., 2007 - portal.acm.org
... this strategy, lnthreads and conventional **multithreading** would occupy ... single-threaded
and **multi- threaded** fetching. ... Figure 9. Single and **multithreaded** fetching ...
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T Ungerer, B Robič, J Šilc - ACM Computing Surveys (CSUR), 2003 - portal.acm.org
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Multi-Threaded Architecture (MTA ... neering example of a **multithreaded** ma- chine ...
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Implicitly-**multithreaded** processors - all 16 versions »
I Park, B Falsafi, TN Vijaykumar - Computer Architecture, 2003. Proceedings. 30th Annual ..., 2003 - ieeexplore.ieee.org
... this paper, we propose the Implicitly-Multi- Threaded (IMT) processor ... We propose
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... Tokens may be sequential (eg **round-robin**), even/odd ... A Survey of Processors with Explicit
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NePSim: a network processor simulator with a power evaluation framework - all 6 versions »
Y Luo, J Yang, LN Bhuyan, L Zhao - Micro, IEEE, 2004 - ieeexplore.ieee.org
... power estimator for NPs consisting of clusters of **multithreaded** execution cores ... Command
bus arbiter Matrix and **round-robin** Orion Three priority requests, six ...
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SW Kekckler, A Chang, W Chatterjee, WJ Dally - Computers, IEEE Transactions on, 1999 - ieeexplore.ieee.org
... EI AL.; CONCURRENT EVENT HANDLING THROUGH **MULTITHREADING** 905 user ... In a **multithreaded**
processor, or one that is heavily ... an event, it must first **wait** for all ...
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A Massively **Multithreaded** Packet Processor - all 2 versions »
S Melvin, M Nemirovsky, E Musoll, J Huynh, R ... - Proc. of NP2, Held in conjunction with HPCA-9, Anaheim, CA, ... 2003 - zytek.com
... **Multithreading** at some level is the only practical way ... Each tribe is in fact a
multithreaded processor with ... packet since no younger packets will **wait** for that ...
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Advanced processor scheduling in a **multithreaded** system
DT Hass, A Rashid - 2005 - freepatentsonline.com
... to take full advantage of 4-way **multithreading**, at full ... CPUs/cores and threads in
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... interval when adopting the **round-robin** policy and ... we can use the simultaneous
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A Fedorova, M Seltzer, MD Smith - Harvard University. Cambridge, MA, Tech. Rep. TR-15-05, 2005 - deas.harvard.edu
... is based on fine-grained **multithreading** (interleaving), proposed ... doing useful work
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E Caspi, A DeHon, J Wawrzynek - Proceedings of the Third Workshop on Media and Stream ..., 2001 - hwswworld.com
... we present SCORE, a scalable, **multi-threaded** computation model ... capacity and a fixed
network **delay** between pages. ... the processor (like memory **wait** states), while ...
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... The **Wait** stage for the ALU and I_MUL ... J. Šilc: A Survey of Processors with Explicit
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... Title: Programmable delayed dispatch in a **multi-threaded** pipeline. ... second value
(use of resource) and third value (**wait** produced by **delay** in dispatch ...
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... 3. A **Multithreaded** Decoupled Architecture ... register operands (Figure 3 right, labelled
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... uni one is granted use and the others must **wait**. ... ideas from research in compile **time**
scheduling, multiple instruction issue architectures, **multi-** ...
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NAS Integer Sort on Multi–threaded Shared Memory Machines *
T Grun, MA Hillebrand - Proc. 4th International Euro-par Conference (Euro-Par'98), 1998 - Springer
... in order to fill all load **delay** slots (see ... The impact of **wait** states on the CPE is
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J Kok - US Patent 6,735,687, 2004 - Google Patents
... is approaching physical limits; accordingly, RC signal **delay** can no ... 2 is a block
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A Ghosh, M Bershteyn, R Casley, C Chien, A Jain, M ... - Design Automation Conference, 1995. Proceedings of the ASP- ... -
ieeexplore.ieee.org
... Currently, most software designers **wait** until a work- ing ... simulator is implemented
as a **multithreaded** program to ... value of minimum and maximum **delay** may report ...
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X Fan - 1994 - tams-www.informatik.uni-hamburg.de
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